1. Add 2.8510 x 103 to 9.8410 x 104, assuming that you have only three significant digits.
2. Perform division for the numbers : -1001 and 11
3. Illustrate the operational concepts of interconnection architecture for the following instructions, Where A,B,C,D are the memory locations of operands residing in memory and R0,R1,R2,R3,R4 are the general purpose registers in the processor.
4. Add A,B,C
5. Add R1,R2,R3
6. Move A, R0
7. Move R4,D